

FDTC 2025: Preliminary Program (PDF)

Schedule refers to Australian Western Std. Time (AWST)

09:00 – 09:10 Welcome and Opening remarks

Keynote I

Chair: TBD

09:10 – 10:00 TBD
Tim Güneysu

Session 1 – FAULT DETECTION AND ANALYSIS

Chair: TBD

10:00 – 10:30 AutoPulse – reproducible discovery of electromagnetic fault injection vulnerabilities
*Ingmar Baumgart, Jonathan Kramer, **Maximilian Staab** and Marek Wehmer*

10:30 – 11:00 Coffee Break

11:00 – 11:30 Fault detection in the control and data-path of neural networks
*Manuel Brosch, Augustin Ewald, Michael Gruber, **Matthias Probst** and Georg Sigl*

11:30 – 12:00 Improving fault vulnerability detection via rehosting and comparative analysis of open-source tools
Shoei Nashimoto

12:00 – 12:30 Fault analysis through body bias injection on the FLASH memory accelerator of a microcontroller
*Florent Bruguier, **Ziling Liao** and Philippe Maurine*

12:30 – 14:10 Lunch

Keynote II

Chair: TBD

14:10 – 15:00 Empowering research on AMD CPUs: open access to advanced fault injection capabilities
Hans Niklas Jacob

Session 2 – FAULT MODELS AND ATTACKS

Chair: TBD

15:00 – 15:30 From NOP to ADD and beyond: a novel fault-model comprising variable-length instruction sets
Xhani Marvin Saß, *Thomas Martin Johannes Lehrach and Jean-Pierre Seifert*

15:30 – 16:00 Coffee Break

- 16:00 – 16:30 Fault attacks against UOV-based signatures
Sven Bauer, *Fabrizio De Santis and Kristjane Koleci*
- 16:30 – 17:00 SIFA on nonce-based authenticated encryption: when does it fail?
Application to Ascon
*Pierre-Louis Cayrel, Vincent Grosso and **Viet-Sang Nguyen***
- 17:30 – 17:15 Closing remarks and Farewell